

The 18th IASTED International Conference on

**PARALLEL AND DISTRIBUTED
COMPUTING AND SYSTEMS**
~PDCS 2006~

November 13 - 15, 2006
Dallas, Texas, USA

FINAL CONFERENCE PROGRAM



LOCATION

Dallas/ Addison Marriott Quorum
14901 Dallas Parkway
Dallas, Texas 75254
USA

PARALLEL AND DISTRIBUTED COMPUTING AND SYSTEMS ~PDCS 2006~

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M.H. Mickle – University of Pittsburgh, USA
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A. Movaghar – Sharif University of Technology, Iran
K. Nakajima – University of Tokyo, Japan
C.J. Nukoon – Assumption University, Thailand
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M. Oudshoorn – Montana State University, USA
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E.H.-M. Sha – University of Texas at Dallas, USA
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L. Sousa – Superior Technical Institute, Portugal
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P. Todorova – Fraunhofer Institute FOKUS, Germany
P. Trunfio – University of Calabria, Italy
C.-W. Tseng – University of Maryland, USA
P. Tvrđik – Czech Technical University, Czech Republic
S. Venkatesan – University of Texas at Dallas, USA
L.N. Vintan – University "Lucian Blaga" of Sibiu, Romania
Y. Wiseman – Bar-Ilan University, Israel
M.-Y. Wu – Shanghai Jiaotong University, PRC
C.-Z. Xu – Wayne State University, USA
L.T. Yang – St. Francis Xavier University, Canada
S.-H. Yang – Loughborough University, UK

K.-M. Yu – Chung Hua University,
Taiwan

J. Zalewski – Florida Gulf Coast
University, USA

W. Zhou – Deakin University,
Australia

X. Zou – Indiana University-Purdue
University Indianapolis, USA

PLEASE NOTE

- ❖ Paper presentations are 15 minutes in length with an additional 5 minutes for questions.
- ❖ Report to your Session Chair 15 minutes before the session is scheduled to begin.
- ❖ Presentations should be loaded onto the presentation laptop in the appropriate room prior to your session.
- ❖ End times of sessions vary depending on the number of papers scheduled.

PROGRAM OVERVIEW

Monday, November 13, 2006

- 07:00 - Registration
08:30 (*Registration Area*)
- 08:30 - Welcome Address
09:00 (*Salon E Room*)
- 09:00 - Keynote Address - "Early Experiences with the Clean Slate Design Approach to Networking Research"
(*Salon E Room*)
- 10:00 - Coffee Break
10:30 (*Grand Ballroom Foyer*)
- 10:30 Session 1 - Special Session 1 - Wireless Mesh Networks
(*Salon A Room*)
- Session 2 - Simulation and Performance Evaluation
(*Salon B Room*)
- 14:00 Session 3 - Scheduling and Resource Allocation
(*Salon A Room*)
- Session 4 - Software Systems
(*Salon B Room*)
- Session 5 - Special Session 2: High Performance Interconnection Networks
(*Salon C Room*)
- 15:00 - Coffee Break
15:30 (*Grand Ballroom Foyer*)
- 15:30 Sessions 3, 4, and 5 Continued

Tuesday, November 14, 2006

- 08:30 - Tutorial I Presentation - "Java for Parallel, Distributed, and Mobile Computing"
(*Salon A Room*)
- Tutorial II Presentation - "Parallel and Distributed Embedded/Real-Time Systems"
(*Salon B Room*)
- 10:00 - Coffee Break
10:30 (*Grand Ballroom Foyer*)
- 10:30 Tutorial I and II Presentations Continued
- 14:00 Session 6 - Scheduling and Load Balancing
(*Salon A Room*)
- Session 7 - Peer-to-Peer Systems
(*Salon B Room*)
- Session 8 - Communication Algorithms and Protocols
(*Salon C Room*)
- Session 9 - Grid and Cluster Computing
(*Salon D Room*)
- 15:00 - Coffee Break
15:30 (*Grand Ballroom Foyer*)
- 15:30 Sessions 6, 7, 8 and 9 Continued
- 18:00 - Cocktail Reception
18:30 Dinner Banquet
(*Salon E - J Rooms*)

Wednesday, November 13, 2006

08:30 Session 10 – Wireless and
Sensor Networks
(*Salon A Room*)

Session 11 – Algorithms
(*Salon B Room*)

Session 12 – Architectures
(*Salon C Room*)

10:00 – Coffee Break

10:30 (*Grand Ballroom Foyer*)

10:30 Sessions 10, 11, and 12
Continued

14:00 Session 13 – Reliability and
Security
(*Salon A Room*)

Session 14 – Web Computing
and Data Mining
(*Salon B Room*)

Session 15 – Grid Computing
(*Salon C Room*)

15:00 – Coffee Break

15:30 (*Grand Ballroom Foyer*)

15:30 Sessions 13, 14 and 15
Continued

MONDAY, NOVEMBER 13, 2006

07:00– 08:30 REGISTRATION

IASTED Staff: M. Deacon (Canada)

Room: Registration Area

08:30 – 09:00 WELCOME

ADDRESS

Presenter: S.Q. Zheng (USA)

Room: Salon E

09:00 – KEYNOTE ADDRESS – “EARLY EXPERIENCES WITH THE CLEAN SLATE DESIGN APPROACH TO NETWORKING RESEARCH”

Presenter: H. Zhang (USA)

Location: Salon E Room

For the past three years, the NSF ITR 100x100 Clean Slate Project has pursued networking research via a clean slate approach by posing the following research question: How would we design the network if we were not bound by existing design decisions and could design it from the scratch? In contrast, the prevailing networking research approach places more emphasis on incremental evolution and backward compatibility.

Dr. Hui Zhang is a professor in the School of Computer Science at Carnegie Mellon University. He is currently leading the 100x100

Clean Slate Project, the 4D Project, and the End System Multicast Project. He has done research on Internet QoS, multicast, and peer-to-peer systems. Algorithms and software packages resulting from his research has been widely adopted by industry and academic institutions.

Professor Zhang was the recipient of the National Science Foundation CAREER Award in 1996 and the Alfred Sloan Fellowship in 2000. He held the CMU SCS Finmeccanica Junior Faculty Chair from 1998 to 2002. He was elected to be an ACM Fellow in 2006. He was the Chief Technical Officer of Turin Networks in 2000-2003.

10:00 – 10:30 COFFEE BREAK

Location: Grand Ballroom Foyer

10:30 – SESSION 1 – SPECIAL SESSION 1: WIRELESS MESH NETWORKS

*Chairs: Y. Zhang (Singapore) and
J. Zheng (USA)*

Room: Salon A

513-809

Wireless Communication Timing Control with Interference Node Detection

Y. Kubo and K. Sekiyama (Japan)

513-810

The Impact of WiFi on the Capacity of Mesh Networks

T. Scherer and T. Engel (Luxembourg)

513-811

A Load-Balanced Channel Assignment Scheme for Wireless Mesh Networks

S. Licking, W.-P. Chen, M. Moh and M. Nguyen (USA)

513-812

Providing End-to-End Fairness in Wireless Mesh Networks with Chain Topologies

C.-W. Hsu, C.-Y. Wang, and T.-C. Hou (Taiwan)

513-813

TETRA Extensions for Building Ad Hoc Networks

P. Plans, J. Paradells, and C. Gomez (Spain)

**10:30 - SESSION 2 -
SIMULATION AND
PERFORMANCE
EVALUATION**

Chairs: J. Drissi (USA) and

H. Che (USA)

Room: Salon B

513-137

A Fast Latency Bound Estimation Algorithm for a Multithreaded Network Processor

H. Che, C. Kumar, and B. Menasinal (USA)

513-174

Experimental Study of the Three Key Issues of Multi-Auctioneer Model in Computer Grid

X. Han, Z. Fang, W. Wang, F. Xu, and M. Huang (PRC)

513-176

Performance Comparison of a Number of Reliable and Fault-Tolerant Hierarchical Interconnection Networks

M. Abd-El-Barr (Kuwait)

513-196

The Efficient Characterization of Adaptive Applications through Fine Profiling based on Data Locality

Q. Liu and K.A. Tomko (USA)

513-219

Advance Bandwidth Reservation Algorithms in Communication Networks: Performance Evaluation

I. Taleb, A. Hafid, and J. Drissi (USA)

513-800

MSOMS: A Meta Simulation Framework for Optimizing Multi-Domain Simulation

M.H. Khan (USA)

**14:00 - SESSION 3 -
SCHEDULING AND
RESOURCE ALLOCATION**

*Chairs: S.M. Elsharkawy (USA) and
J.B. Pedersen (USA)
Room: Salon A*

513-023
Wavelength Assignment for
Broadcasting in Sparse
Conversion Tree Networks with
Fewest Converters
T. Yi (USA)

513-049
Improved Asynchronous Group
Mutual Exclusion in
Token-Passing Networks
*D. Lin, T.-S. Moh, and M. Moh
(USA)*

513-059
Approximating the Buffer
Allocation Problem using Epochs
*J.B. Pedersen (USA) and A. Brodsky
(Canada)*

513-089
A Resource Management System
for Data-Intensive Applications in
Desktop Grid Environments
*T. Toyama, Y. Yamada, and
K. Konishi (Japan)*

513-147
Communication-Conscious
Mapping of Regular Nested Loop
Programs onto Massively Parallel
Processor
Arrays
*S. Siegel, R. Merker, F. Hannig and
J. Teich (Germany)*

513-172
Power-Aware Parametric
Dispatching in Distributed
Real-Time Systems
*S.M. Elsharkawy, M.F. Younis, and
C. Moulds (USA)*

513-181
Service-Aware Virtual Machine
Placement Mechanism for Utility
Computing
*M. Kawato, F. Machida, and
Y. Maeno (Japan)*

**14:00 - SESSION 4 - SOFTWARE
SYSTEMS**

*Chair: M. Quinson (France)
Room: Salon B*

513-035
Design of the SAgent Security
Framework for JADE
V. Gunupudi and S.R. Tate (USA)

513-044
Model-based Design of the
Communication System in an
Integrated Architecture
*R. Obermaisser and B. Huber
(Austria)*

513-091
Analysis of Software-based
Recovery Schemes for SMT
Processors
*L. Beyer, B. Fechner, and J. Keller
(Germany)*

513-098

How to Improve the Scalability of an Industrial Parallel Reservoir Simulator

*J.-M. Gratien, T. Guignon,
J.-F. Magras, P. Quandalle, and
O. Ricois (France)*

513-102

GRAS: A Research and Development Framework for Grid and P2P Infrastructures

M. Quinson (France)

513-103

DisUnit: Enable Automated Distributed Testing for Distributed Systems

*R. Qu, S. Hirano, T. Ohkawa,
T. Kubota (Japan), and R. Nicolescu
(New Zealand)*

513-124

Memory Utilization Analysis of Java Middleware for Distributed Real-Time and Embedded Systems

*R. Qu, S. Hirano, and T. Ohkawa
(Japan)*

513-218

cmUML - A Precise UML for Abstract Specification of Concurrent Components

*J. Suryadevara and
R.K. Shyamasundar (India)*

**14:00 – SESSION 5 – SPECIAL
SESSION 2: HIGH
PERFORMANCE
INTERCONNECTION
NETWORKS**

Chairs: E. Lu (USA)

Room: Salon C

513-814

Modeling Scale-Free Networks with Heterogeneous Nodes

M. Song and S. Shetty (USA)

513-815

Dynamic Buffer Allocation for Quality of Service Provision in Combined Input and Crosspoint Buffered Switches

Q. Duan and Y. Zhou (USA)

513-816

Optimization of Communication Cost within Processor Arrays Caused by I/O

S. Siegel and R. Merker (Germany)

513-817

A Networking Structure Favorable for All-Optical Packet Switching

S. Jiang (PRC)

513-818

Balancing Load using a New Load Metric

J. Liu (USA)

513-819

Generalized Wavelength Routed
Optical Micronetwork in
Network-on-Chip

*L. Zhang, M. Yang, Y. Jiang,
E. Regentova, and E. Lu (USA)*

513-820

A Low-Cost Almost-Nonblocking
Switching Network

H. Yu and S.Q. Zheng (USA)

15:00 - 15:30 COFFEE BREAK

Location: Grand Ballroom Foyer

15:30 - SESSIONS 3, 4, and 5 CONTINUED

TUESDAY, NOVEMBER 14, 2006

08:30 – TUTORIAL I PRESENTATION– “JAVA FOR PARALLEL, DISTRIBUTED, AND MOBILE COMPUTING”

Presenter: S. Chaumette (France)

Room: Salon A

The goal of this tutorial is to provide the attendees with the knowledge necessary to decide whether or not to use the Java programming language to develop distributed/mobile applications. The attendees will know how to write network/parallel code in Java, how to migrate mobile codes in

Java, and how to write dynamically upgradeable codes in Java. More importantly they will be able to decide if Java is the right answer to their lab/production problems

Dr. Serge Chaumette has been working with Java since 1993. He is the organizer (with Geoffrey Fox, Jack Dongarra, and Denis Caromel) of the International Workshop on Java for Parallel and Distributed Programming that takes place every year jointly with IPDPS. He is leading the Distributed Systems and Objects Team of the Laboratoire Bordelais de Recherche en Informatique at the University Bordeaux 1, France. He directs a Masters program specialized in Parallel and Distributed Programming and leads or is involved with many projects funded by national agencies. Serge is a member of the WG 8.8 « Smart Card » of the IFIP (International Federation For Information Processing), and a member of the IEEE.

**08:30 – TUTORIAL II
PRESENTATION– “PARALLEL
AND DISTRIBUTED
EMBEDDED/REAL-TIME
SYSTEMS”**

Presenter: A. Cheng (USA)

Room: Salon B

Real-time or embedded computer systems must satisfy stringent response time constraints in addition to logical correctness constraints. Parallel and distributed computer systems research and development has produced systems capable of attaining very high performance in terms of speed and versatility at very attractive cost-to-speed ratio. Furthermore, the approach of parallel and distributed computing is a natural way to surpass the limiting device technology in conventional uniprocessor systems.

Dr. Albert M. K. Cheng received the B.A. with Highest Honors (Phi Beta Kappa) in Computer Science, the M.S. in Computer Science with a minor in Electrical Engineering, and the Ph.D. in Computer Science, all from The University of Texas at Austin, where he held a GTE Foundation Doctoral Fellowship. Dr. Cheng is currently a tenured Associate Professor in the Department of Computer Science at the University of Houston, where he is the founding Director of the Real-Time Systems Laboratory. He has

served as a technical consultant for several organizations, including IBM, and was a visiting faculty at Rice University (2000) and at the City University of Hong Kong (1995).

Dr. Cheng has authored/coauthored over 100 refereed publications. He has been invited to present seminars, tutorials, and keynotes at over 60 conferences and organizations. He was an Associate Editor of the IEEE Transactions on Software Engineering (1998-2003), and is an Associate Editor of the Journal of Embedded Systems and the Journal of Computer and Information Science.

10:00 – 10:30 COFFEE BREAK

Location: Grand Ballroom Foyer

**10:30 – TUTORIALS I AND II
CONTINUED**

**14:00 - SESSION 6 -
SCHEDULING AND LOAD
BALANCING**

*Chairs: M. Moges (USA) and
K.F. Chen (USA)*

Room: Salon A

513-038

A Meta-Algorithm for Scheduling
Multiple DAGs in Homogeneous
System Environments

*U. Hönig and W. Schiffmann
(Germany)*

513-054

Global Static-Priority Scheduling
of Sporadic Task Systems on
Multiprocessor Platforms

N. Fisher and S. Baruah (USA)

513-066

Task Redistribution Scheduling
using Multi-Master Divisible Load
Model

R. Suda and S. Tomi (Japan)

513-088

Iterative Algorithms for
Stochastically Robust Static
Resource Allocation in Periodic
Sensor Driven Clusters

*V. Shestak, J. Smith, A.A.
Maciejewski, and H.J. Siegel (USA)*

513-094

Efficient Support of Fine-Grained
Futures in Java

*L. Zhang, C. Krintz, and S. Soman
(USA)*

513-136

Integrated Scheduling Algorithm
for Sensor Networks based on
Divisibility Theory

X. Yuan and M. Moges (USA)

513-151

QoS Guarantee in Input-Queued
Switches with Noniterative
Schedulers

*K.F. Chen, E.-H.-M. Sha, and
S.Q. Zheng (USA)*

513-206

O(1) Runtime Contour-based
Priority (CBP) Scheduling in
Optical Burst Switched Networks

*Y. Chen, J.S. Turner, and Z. Zhai
(USA)*

**14:00 - SESSION 7 -
PEER-TO-PEER SYSTEMS**

Chair: M. Song and M. Abdallah

Room: Salon B

513-030

Efficient Support of Conjunctive
Queries in P2P DHT by using
Bloom Filter

*K. Kabatake, S. Tagashira, and
S. Fujita (Japan)*

513-062

End-to-End Arguments and
DHT-System Design

X. Li and M. Brockmeyer (USA)

513-078

Simple and Efficient Replication
in Chord

*M. Christodoulidou and P. Fatourou
(Greece)*

513-152

A P2P Reputation System with a
Fuzzy Technique for Incentive
Provision

*Y.-K. Lin and M.-C. Huang
(Taiwan)*

513-200

RHT: Supporting Range Queries
in DHT-based P2P Systems

L. Liu and K.D. Ryu (USA)

513-216

GODIS: Ontology-based Resource
Discovery and Integration in
Grids

*J. Li, I. Radu, and S.T. Vuong
(Canada)*

513-803

Efficient Routing in Non-Uniform
DHTs for Range Query Support

*M. Abdallah and E. Buyukkaya
(France)*

**14:00 – SESSION 8 –
COMMUNICATION
ALGORITHMS AND
PROTOCOLS**

Chair: *J.A. Cobb (USA)*

Room: *Salon C*

513-040

Eco-Friendly Thermal Aware
Routing Protocol for Habitat
Monitoring

A. Bag and M.A. Bassiouni (USA)

513-051

Set-to-Set Disjoint Paths Routing
in Pancake Graphs

S. Peng and K. Kaneko (Japan)

513-064

NPP: Nested Ping-Pong
Scheduling, Proportional Share
Scheduling Algorithm by
Ordering the Scheduling
Quantum

S. Baek (USA)

513-142

On the Complexity of Channel
Assignment for Real-Time Flows

J.A. Cobb (USA)

513-154

Adaptive QoS Management in
Heterogeneous Home Network
Applications

*M.-C. Chen, J.-L. Chen, Y.-C. Chang,
and Y.-R. Chian (Taiwan)*

513-207
Continuous Delivery Message
Dissemination Problems under
the Multicasting Communication
Mode
T.F. Gonzalez (USA)

513-806
BPA-A Parallel Shortest Path
Algorithm for Cluster-Router
*X. Zhang, J. Wu, N. Zhang, and
Y. Zhao (PRC)*

513-808
QoS Assuring Access Control
Protocols for Hyperchannels in
SMART Network
*J. Chen, S.Q. Zheng (USA), and
A. Gumaste (India)*

14:00 - SESSION 9 - GRID AND CLUSTER COMPUTING

*Chairs: S.S. Kadam (India) and
J. Qin (Canada)*
Room: Salon D

513-076
Queueing Theory Approach to
Allocating QoS-Constrained
Workflow-based Applications in a
Web Service-based Grid
Y. Patel and J. Darlington (UK)

513-085
Preemptive Process Migration in a
Cluster of Non-Dedicated
Workstations
*M. Kačer, J. Čapek, and P. Tordík
(Czech Republic)*

513-095
A Simulator for Job Co-Allocation
in Multiple HPC Clusters
J. Qin and M.A. Bauer (Canada)

513-141
Cluster-based Parallelization of
Multi-Scale Active Shape
Description Process
S.S. Kadam (India)

513-149
FastPara: A High-Level
Declarative Data-Parallel
Programming Framework on
Clusters
*Y. Mao, Y. Gu, J. Chen, and
R.L. Grossman (USA)*

513-175
A Broker and Job Advertisement
based Grid Scheduling
Framework
*A.T. Thor, G.V. Záruba, and
D. Levine (USA)*

513-208
Evaluation of Heterogeneous
Nodes in a Nondedicated Cluster
*M. Košťál and P. Tordík
(Czech Republic)*

15:00 - 15:30 COFFEE BREAK
Location: Grand Ballroom Foyer

**15:30 - SESSIONS 6, 7, 8, and 9
CONTINUED**

18:00 – COCKTAIL RECEPTION

18:30 – DINNER BANQUET

Room: Salons E – J

**WEDNESDAY,
NOVEMBER 15, 2006**

**08:30 – SESSION 10 - WIRELESS
AND SENSOR NETWORKS**

*Chairs: X. Chen (USA) and
Y. Chen (USA)*

Room: Salon A

513-026

MCORE: A Simple Structure for
Effective Overlay Multicast on
Mobile Ad Hoc Networks

Y. Li, S. Peng, and W. Chu (Japan)

513-053

Dynamic Resource-Constrained
Service Composition for Mobile
Ad Hoc Networks

*G. Kaefer, R. Schmid (Germany),
G. Prochart, and R. Weiss (Austria)*

513-081

Optimal Configuration of
Clustering Protocols for Sensor
Networks

P. Banerjee and D. Jacobson (USA)

513-138

An Efficient MAC Protocol for
Wireless Ad-Hoc Networks

*S. Park, P.J. Weber, R. Jayaraman,
and L.C. Robles Felix (USA)*

513-171

Locating Data Servers in Ad Hoc
Mobile Networks with K-Hop
Time Constraint

X. Chen (USA)

513-187

Cross Layer Optimization in
Wireless Sensor Network with
Rate Distribution

*H. Wang, D. Peng, W. Wang, and
H. Sharif (USA)*

513-192

On the Short-Term Feasibility of
using RFID in Realizing Ambient-
Intelligence in Indian Homes

*M.B. Mariappan, M.S.
Ambasamudram Sailappan,
M. Chidambaram, and
R.V. Uthariaraj (India)*

513-109

Cost Effective Non-Wavelength-
Converting Multi-Lane Optical
Burst Switching (ML-OBS)

Y. Chen (USA)

513-210

Controlling the Expiration
Sequence of Sensor Network
Cluster Heads using Genetic
Algorithm

Z. Zhai and Y. Chen (USA)

**08:30 – SESSION 11 –
ALGORITHMS**

*Chairs: G. Cong (USA) and
Z.J. Czech (Poland)
Room: Salon B*

513-006

An Evaluation of Parallel
Algorithms on Current Memory
Consistency Models
G. Cong (USA)

513-022

Implementation of Surface-to-
Surface Contact Algorithms using
Iterative Solvers on Parallel
Computers
*E.J. Kim (Korea) and T.A. Laursen
(USA)*

513-043

Co-operation of Processes in
Parallel Simulated Annealing
Z.J. Czech (Poland)

513-079

A Distributed Optimization
Approach to Ubiquitous Display
Environments
*T. Heider, M. Giersich, and T. Kirste
(Germany)*

513-106

Experiments with Strassen's
Algorithm: From Sequential to
Parallel
*F. Song, J. Dongarra, and S. Moore
(USA)*

513-156

An Efficient Parallel Algorithm
for Finding the (K, L) -Center of
Tree Networks
*D. Wang, Y. Li, Y. Wang, and
K. Wang (PRC)*

513-162

Repeat Analysis in DNA
Sequences with Parallel
Computing Method on Grid
*A. Arefin, A.I. Anwar, and A. Wasif
(Bangladesh)*

513-198

UMR2: A Better and More
Realistic Scheduling Algorithm
for the Grid
*T.L. Nguyen (Japan), S. Elnaffar
(UAE), T. Katayama, and H.T. Bao
(Japan)*

**08:30 – SESSION 12 –
ARCHITECTURES**

*Chairs: Jung (USA) and Yokota (Japan)
Room: Salon C*

513-034

Parallelization of Multimedia
Applications on the Multi-Level
Computing Architecture
*U. Aydonat and T.S. Abdelrahman
(Canada)*

513-067

Entropy Properties in Program
Behaviors and Branch Predictors
*T. Yokota, K. Ootsu, F. Furukawa,
and T. Baba (Japan)*

513-086

Solving the Longest Common Subsequence (LCS) Problem using the Associative ASC Processor with Reconfigurable 2D Mesh

S.S. Viridi, H. Wang, and R.A. Walker (USA)

513-087

Implementing a Multiple-Instruction-Stream Associative MASC Processor

H. Wang and R.A. Walker (USA)

513-093

Communication Reduction Techniques in Multiple Multicasts for 3D Mesh and Torus Networks

D.R. Surma (USA)

513-183

A Programmable Instruction Decoder for Heterogeneous Multiprocessor Architectures

Y.-K. Jung (USA)

513-201

String Matching Engine using Parallel Hashing

P. Katta, M. Nourani, and R. Panigrahy (USA)

513-217

Lower Level Architecture of the Sombrero Single Address Space Distributed Operating System

D.S. Miller, D.B. White, A.C. Skousen, and R. Tcherepov (USA)

10:00 – 10:30 COFFEE BREAK

Location: Grand Ballroom Foyer

10:30 – SESSIONS 10, 11 AND 12 CONTINUED

14:00 – SESSION 13 – RELIABILITY AND SECURITY

Chair: M. Kucera (Germany)

Room: Salon A

513-020

Vertex-Disjoint Paths in Transposition Graphs

S. Fujita (Japan)

513-084

Semi-Automatic Reliability Assessment of Safety Related Embedded Systems

M. Kucera and H. Mauser (Germany)

513-105

Dynamic Evolution in a Survivable Application Infrastructure

H.D. Thorvaldsson and K.J. Goldman (USA)

513-127

MAT6: A Hybrid Address Autoconfiguration in IPv6 Networks

S.-K. Tang, K.-H. Yeung, and K.-Y. Wong (PRC)

513-177
Deterministic Multithreading for
Java-based Replicated Objects
*J. Domaschka, F.J. Hauck,
H.P. Reiser, and R. Kapitza
(Germany)*

513-212
I/O Node Placement for
Performance and Reliability in
Torus Networks
*B. Azeez, H. Kim, Y. Jin, and
E.J. Kim (USA)*

513-215
Byzantine Fault Tolerant
Execution of Long-Running
Distributed Applications
*S.L. Pallemulle, I. Wehrman, and
K.J. Goldman (USA)*

513-807
INTESER: An Integrated Solution
to Provide QoS, Traffic
Engineering and Fault Tolerance
in an MPLS Network
*H. Che, M. Gupta, S. Velayutham,
C. Lagoa, and Z. Wang (USA)*

14:00 - SESSION 14 - WEB COMPUTING AND DATA MINING

*Chair: M. Qiu (USA)
Room: Salon B*

513-046
SLINC: A Framework for
Volunteer Computing
*J. Baldassari, D. Finkel, and D. Toth
(USA)*

513-073
Backup and Recovery Mechanism
for a Distributed e-Learning
System
*T. Kawamura, S. Kinoshita,
S. Motomura, and K. Sugahara
(Japan)*

513-075
Combination of XML-RPC and
Mobile Agent Technologies
*S. Motomura, T. Kawamura, and
K. Sugahara (Japan)*

513-108
Suitability of Overlays as a
General-Purpose Data
Communication Substrate on
Gigabit Channels
*J. Quaini-Sousa, M.A. de Lima e
Silva, F. Redigolo, T.C. Carvalho,
W. Ruggiero, H. Guardia, and
B. Ohlman (Brazil)*

513-112
Performance of Dynamic Web
Page Generation for e-Business
Web Sites
*P. Ghosh and A. Rau-Chaplin
(Canada)*

513-146
Loop Scheduling to Minimize
Cost with Data Mining and
Prefetching for Heterogeneous
DSP
*M. Qiu (USA), Z. Jia (PRC), C. Xue
(USA), Z. Shao (PRC), Y. Liu and
E.H.-M. Sha (USA)*

513-191
Probabilistic Load Balancing
Method for Parallel Mining of all
Frequent Itemsets
R. Kessl and P. Tvrđík
(Czech Republic)

**14:00 - SESSION 15 - GRID
COMPUTING**

*Chairs: A.C. Sodan (Canada) and
S. Baskiyar (USA)*
Room: Salon C

513-027
A Two-Phase Scheduling
Approach for Grid Computing
F. Dong and S.G. Akl (Canada)

513-083
Investigating the Replica Transfer
Scheduling Problem
*T. Loukopoulos, N. Tziritas,
P. Lampsas, and S. Lalis (Greece)*

513-090
A Distributed Peer-to-Peer Grid
Scheduler
*C. Liu, S. Baskiyar, and C. Wang
(USA)*

513-122
A Case Study on Grid
Performance Modeling
*B. Lu, A. Apon, L. Dowdy,
F. Robinson, D. Hoffman, and
D. Brewer (USA)*

513-135
A Case for Grid based Video on
Demand System
A. Chakrabarti (India)

513-199
Evaluation of Replication and
Rescheduling Heuristics for Grid
Systems with Varying Resource
Availability
*M. Chtepen, B. Dhoedt, F. De Turck,
P. Demeester, F.H.A. Claeys, and
P.A. Vanrolleghem (Belgium)*

513-209
ATOP-Grid for Unified
Multidimensional Adaptation of
Grid Applications
A.C. Sodan and G. Gupta (Canada)

15:00 - 15:30 COFFEE BREAK
Location: Grand Ballroom Foyer

**15:30 - SESSIONS 13, 14, and 15
CONTINUED**

**IASTED would like to thank you
for attending PDCS 2006. Your
participation helped make this
international event a success, and
we look forward to seeing you at
upcoming IASTED events.**
